



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Eloranta et al.

Attorney Docket: 944-003.189

Serial No.: 10/717,986

Group Art Unit: to be assigned

Filed: November 20, 2003

Examiner: to be assigned

For: **METHOD AND DEVICE FOR DIGITAL-TO-RF CONVERSION**

Director of the U.S. Patent & Trademark Office
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INFORMATION DISCLOSURE STATEMENT

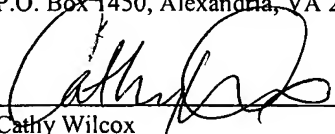
Sir:

Applicant submits herewith references of which they are aware, which they believe may be material to the examination of this application and in respect of which they may have a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any document referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined under 37 CFR 1.56(a) exists.

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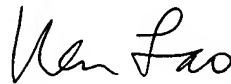

Cathy Wilcox

2.19.04
Date

A PTO-1449 with cited reference is also enclosed.

This IDS is being submitted prior to receipt of a first Official Action in this matter; therefore, the undersigned respectfully submits that no fee is due for filing this IDS. Should any fees be due of which the undersigned is unaware, the Commissioner is hereby authorized to charge deposit account 23-0442 any fee deficiency required to submit this IDS.

Respectfully submitted,



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Date: Jul. 19, 2004

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FORM PTO-1449
INFORMATION DISCLOSURE STATEMENT

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UNITED STATES PATENT DOCUMENTS

EXAM. INITIAL	DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
WO 02/37667	May 10, 2002	International			

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

	IEEE Journal of Solid-State Circuits 0018-9200/02, Vol. 37, No. 9; Gutierrez-Aitken et al.; "Ultrahigh-Speed Direct Digital Synthesizer Using InP DHBT Technology"; pp. 1115-1119; September 2002.
	IEEE 2001 Custom Integrated Circuits Conference 0/7803/6591-7/01; Splett et al.; "Solutions for highly integrated future generation software radio basestation transceivers"; pp. 511-518; 2001.
	IEEE 0-7803-3625-9/96; Koli et al.; "Current-Mode Up-Converting D/A-Interface for Mobile Communication Transmitter Applications"; pp. 20-24; 1996.
	IEEE 0018-9200/03; Zhou et al.; "A 10-Bit Wide-Band CMOS Direct Digital RF Amplitude Modulator"; pp. 1182-1188; 2003.

Examiner (To be assigned)

Date: